

### Lesson Plan

**Name of the Faculty** : Er. Monika Sharma (Theory/Practical)  
**Discipline** : Electronics and Communication Engineering  
**Semester** : 4<sup>th</sup>  
**Subject:** Microprocessors and Microcontroller (EC-210A)  
 Microprocessors and Microcontroller Lab (EC-212LA)

**Lesson Plan Duration** : 15 weeks (from Jan, 2021 to Apr, 2021)

**Work Load (Lecture / Practical) per week (in hours):** Lectures-03, Practical-03

Week	Theory		Practical	
	Lecture Day	Topic (including assignment / test)	Practical Day	Topic
1 <sup>st</sup>	1 <sup>st</sup>	Evolution of Microprocessor	1 <sup>st</sup>	Add / Sub two 16 bit numbers.
	2 <sup>nd</sup>	Introduction to 8-bit Microprocessor 8085 architecture		
	3 <sup>rd</sup>	Pin Details 8085 Microprocessor		
2 <sup>nd</sup>	4 <sup>th</sup>	8086 Architecture description of data registers, address registers	2 <sup>nd</sup>	Multiply two 16 bit unsigned/ signed numbers.
	5 <sup>th</sup>	Pointer and index registers		
	6 <sup>th</sup>	PSW, Queue BIU and EU		
3 <sup>rd</sup>	7 <sup>th</sup>	8086 Pin diagram descriptions	3 <sup>rd</sup>	Divide two unsigned/ signed numbers (32/16 , 16/8, 16/16, 8/8 )
	8 <sup>th</sup>	Generating 8086 CLK and reset signals using 8284		
	9 <sup>th</sup>	WAIT state generation.		
4 <sup>th</sup>	10 <sup>th</sup>	Microprocessor BUS types and buffering techniques	4 <sup>th</sup>	Find smallest/ largest number from array of n numbers.
	11 <sup>th</sup>	8086 minimum mode and maximum mode CPU module		
	12 <sup>th</sup>	Assignment-1/ Class Test		
5 <sup>th</sup>	13 <sup>th</sup>	8086 CPU Read/Write timing diagrams in minimum mode and maximum mode.	5 <sup>th</sup>	First Internal Viva
	14 <sup>th</sup>	8051 Architecture		

	15 <sup>th</sup>	On-chip memory organization – general purpose registers		
6 <sup>th</sup>	16 <sup>th</sup>	SFR registers, Internal RAM and ROM	6 <sup>th</sup>	Arrange numbers in array in ascending/ descending order.
	17 <sup>th</sup>	Oscillator and Clock circuits		
	18 <sup>th</sup>	Pin Diagram of 8051		
7 <sup>th</sup>	19 <sup>th</sup>	I/O Pins, Port, Connecting external memory	7 <sup>th</sup>	Convert Hex to Decimal, Decimal to Hex.
	20 <sup>th</sup>	Counters and Timers, Purpose of TCON & TMOD registers		
	21 <sup>st</sup>	Serial data transmission/reception and transmission modes		
8 <sup>th</sup>	22 <sup>nd</sup>	Purpose of SCON & PCON registers.	8 <sup>th</sup>	Compare two strings using string instructions / without using string instructions.
	23 <sup>rd</sup>	Assignment-2/ Class Test		
	24 <sup>th</sup>	Different Types of Interrupts		
9 <sup>th</sup>	25 <sup>th</sup>	Purpose of Time Delays	9 <sup>th</sup>	Second Internal Viva
	26 <sup>th</sup>	8051 addressing modes		
	27 <sup>th</sup>	8086 Instruction format		
10 <sup>th</sup>	28 <sup>th</sup>	Addressing modes,	10 <sup>th</sup>	Display string in reverse order, string length, Concatenation of two strings.
	29 <sup>th</sup>	Data transfer instructions		
	30 <sup>th</sup>	String instructions, logical instructions		
11 <sup>th</sup>	31 <sup>st</sup>	Arithmetic instructions, transfer of control instructions	11 <sup>th</sup>	To find 1's and 2's complement of a number.
	32 <sup>nd</sup>	Process control instructions.		
	33 <sup>rd</sup>	8051 Data transfer instructions		
12 <sup>th</sup>	34 <sup>th</sup>	Arithmetic and logical instructions	12 <sup>th</sup>	To find 1's and 2's complement of a number.
	35 <sup>th</sup>	Jump and Call instructions I/O port		
	36 <sup>th</sup>	Timer and Counter programming		
13 <sup>th</sup>	37 <sup>th</sup>	Serial port and Interrupt programming	13 <sup>th</sup>	To find Factorial of a number.

	38 <sup>th</sup>	Assembly language programs.		
	39 <sup>th</sup>	Memory devices, Address decoding techniques		
14 <sup>th</sup>	40 <sup>th</sup>	Interfacing SRAMS, ROMS /PROMS	14 <sup>th</sup>	To write an ALP using 8051 Microcontrollers to perform multi byte addition and subtraction of unsigned number .
	41 <sup>st</sup>	8086 Interrupt mechanism, Interrupt types and interrupt vector table		
	42 <sup>nd</sup>	Intel's 8255 - description and interfacing with 8086		
15 <sup>th</sup>	43 <sup>rd</sup>	ADCs and DACs, - types operation and interfacing with 8086. Interfacing of Matrix Keyboards	15 <sup>th</sup>	Third Internal Viva
	44 <sup>th</sup>	ADC, DAC, Temperature Sensor, Stepper Motor with 8051		
	45 <sup>th</sup>	Assignment-4/ Class Test		

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